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APPLICANTS

Tzu-En Ho, Ilan, TAIWAN;

Chih-Hao Chang, Taipei, TAIWAN;
Chang-Rong Wu, Taipei, TAIWAN;Kuo-Hui Su, Taipei, TAIWAN;** CONTINUING DATA ***** *none* JK** FOREIGN APPLICATIONS *****
TAIWAN 92120045 07/23/2003 *yes* *JK*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 01/12/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY TAIWAN	SHEETS DRAWING 3	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 2
Verified and Acknowledged Examiner's Signature <i>J. Reuledy</i>	Initials <i>JK</i>				

ADDRESS

34283
 QUINTERO LAW OFFICE
 1617 BROADWAY, 3RD FLOOR
 SANTA MONICA, CA
 90404

TITLE

Method for fabricating semiconductor device having stacked-gate structure

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